

PROGRAMMABLE POWER MANAGEMENT SYSTEM AND METHOD

ABSTRACT OF THE INVENTION

A programmable power management integrated circuit includes analog input
5 monitors that receive analog input signals that correspond to voltage, current, or
temperature measurements. The analog input monitors apply programmable
thresholds to the measurements and output the results to a programmable logic
device, which may generate various status and/or control signals to the system being
monitored. The programmable logic device controls FET drivers that can switch on
10 and off power to the monitored system. The programmable power management
integrated circuit may also comprise an internal oscillator, a serial interface, an in-
system programmable interface, a joint test action group interface, a memory that
stores identification information, and a register for capturing system information
during power-down.